AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1.-71. (Canceled)

72. (New) An electronic device comprising:

a display having a memory effect, the display adapted to maintain an image thereon without receiving electric power;

a driving circuit adapted to perform image writing on the display;

a data processing unit which is connected to the driving circuit, the data processing unit including at least one central processing unit adapted to operate in a sleep state during which part of an internal circuit is inactivated after completion of image writing;

a power supply circuit adapted to supply electric power to the driving circuit, the power supply circuit including one element selected from the group consisting of a booster circuit and a DC/DC converter;

a data reader including a slot adapted to receive a data storage medium, the data reader adapted to read data stored on a received data storage medium;

an operation section adapted to receive an input from a user; and

a controller adapted to inactivate at least one part of the power supply circuit after completion of image writing, the controller adapted to reactivate the inactivated at least one part of the power supply circuit upon receiving an input from the operation section;

wherein the at least one central processing unit changes from the sleep state to an active state and the data reader reads data from a data storage medium received in the data reader when the controller receives an input from the operation section,

wherein the controller reactivates the inactivated at least one part of the power supply circuit after the data reader reads data from a data storage medium,

wherein the driving circuit performs image writing on the display after the

inactivated at least one part of the power supply circuit is reactivated, and

wherein the controller inactivates the at least one part of the power supply circuit after completion of image writing, the at least one central processing unit subsequently changes to the sleep state, and the display maintains an image thereon without receiving electric power.

73. (New) An electronic device according to claim 72,

wherein the at least one central processing unit changes from the sleep state to the active state when a data storage medium is installed in the data reader,

wherein the data reader reads data from a data storage medium installed in the data reader,

wherein the controller reactivates the inactivated at least one part of the power supply circuit after the data reader reads the data,

wherein the driving circuit performs image writing on the display after the inactivated at least one part of the power supply circuit is reactivated,

wherein the controller inactivates the at least one part of the power supply circuit after completion of image writing, and

wherein the at least one central processing unit changes to the sleep state after the controller inactivates the at least one part of the power supply circuit.

74. (New) An electronic device according to claim 72, wherein:

the operation section includes a page forward key or a page backward key; and

the driving circuit performs image writing of a next page or a previous page in response to an input from the operation section.

75. (New) An electronic device according to claim 72, wherein the display includes a liquid crystal which exhibits a cholesteric phase.

76. (New) An electronic device according to claim 72, wherein inactivation of the at least one part of the power supply circuit is inhibited while an input is being continuously made with the operation section.

77. (New) An electronic device according to claim 72, wherein the data processing unit further includes a second central processing unit adapted to remain in an active state when the at least one central processing unit is in the sleep state.

78. (New) An electronic device according to claim 72, wherein the data processing unit further includes a second central processing unit, wherein power consumption of the second central processing unit is smaller than power consumption of the at least one central processing unit.

79. (New) An electronic device comprising:

a display having a memory effect, the display adapted to maintain an image thereon without receiving electric power;

a driving circuit adapted to perform image writing on the display;

a data processing unit which is connected to the driving circuit, the data processing unit including at least one central processing unit adapted to operate in a sleep state during which part of an internal circuit is inactivated after completion of image writing;

a power supply circuit adapted to supply electric power to the driving circuit, the power supply circuit including one element selected from the group consisting of a booster circuit and a DC/DC converter:

a data reader including a slot adapted to receive a data storage medium, the data reader adapted to read data stored on a received data storage medium;

an operation section adapted to receive an input from a user; and

a controller adapted to inactivate at least one part of the power supply circuit after completion of image writing, the controller adapted to reactivate the inactivated at least one part of the power supply circuit upon receiving an input from the operation section;

wherein the at least one central processing unit changes from the sleep state to an active state when the controller receives an input from the operation section,

wherein the data reader reads data from a data storage medium installed in the data reader and the driving circuit performs image writing on the display after the at least one central processing unit changes to the active state,

wherein the controller reactivates the inactivated at least one part of the power supply circuit before image writing and inactivates the at least one part of the power supply circuit after completion of image writing, and

wherein data reading and image writing are not performed simultaneously.

80. (New) An electronic device according to claim 79,

wherein the at least one central processing unit changes from the sleep state to the active state when a data storage medium is installed in the data reader,

wherein the data reader reads data from a data storage medium installed in the data reader,

wherein the controller reactivates the inactivated at least one part of the power supply circuit after the data reader reads the data,

wherein the driving circuit performs image writing on the display after the inactivated at least one part of the power supply circuit is reactivated,

wherein the controller inactivates the at least one part of the power supply circuit after completion of image writing, and

wherein the at least one central processing unit changes to the sleep state after the controller inactivates the at least one part of the power supply circuit.

81. (New) An electronic device according to claim 79, wherein:

the operation section includes a page forward key or a page backward key; and

the driving circuit performs image writing of a next page or a previous page in response to an input from the operation section.

82. (New) An electronic device according to claim 79, wherein the display includes a liquid crystal which exhibits a cholesteric phase.

83. (New) An electronic device according to claim 79, wherein inactivation of the at least one part of the power supply circuit is inhibited while an input is being continuously made with the operation section.

84. (New) An electronic device according to claim 79, wherein the data processing unit further includes a second central processing unit adapted to remain in an active state when the at least one central processing unit is in the sleep state.

85. (New) An electronic device according to claim 79, wherein the data processing unit further includes a second central processing unit, wherein power consumption of the second central processing unit is smaller than power consumption of the at least one central processing unit.

86. (New) A method for operating a display apparatus comprising:

receiving a user input;

changing a processor from a sleep state to an active state when the processor receives a user input;

reading data from a data storage medium after changing the processor to the active state;

activating at least one portion of a power supply circuit after reading data from a data storage medium;

writing an image on a display portion of the display apparatus;

inactivating the at least one portion of the power supply circuit after writing an image on the display portion; and

changing the processor from the active state to the sleep state after inactivating the at least one portion of the power supply circuit,

wherein the display portion maintains an image thereon without receiving electric power.

87. (New) A method according to claim 86, wherein a user input corresponds to a user installing a data storage medium in the display apparatus.

88. (New) A method according to claim 86, wherein a user input corresponds to a user pressing a page forward key or a page backward key.

89. (New) A method according to claim 86, wherein the display portion includes a liquid crystal which exhibits a cholesteric phase.

90. (New) A method according to claim 86, further comprising inhibiting inactivating the at least one portion of the power supply when a user input is being continuously received.

91. (New) A method according to claim 86, wherein the processor includes a first processing portion and a second processing portion, the first processing portion consuming less power when the processor is in the sleep state than when the processor is in the active state, the second processing portion remaining in an active state when the processor is in the sleep state.